Refine Search

Search Results -

Terms	Documents
(memory near2 controller) same (switch$3 near5 path) same (enabl$3 or disabl$3)	21

Database:

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search: L3

[Refine Search]

[Recall Text] [Clear] [Interrupt]

Search History

DATE: **Tuesday, April 27, 2004**   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L3	(memory near2 controller) same (switch$3 near5 path) same (enabl$3 or disabl$3)	21	L3
L2	(memory near2 controller) same (switch$3 near5 path) same enabl$3	21	L2
L1	(memory near2 controller) same (switch$3 near5 path) same enabl$3 same disabl$3	0	L1

END OF SEARCH HISTORY

h   e   b   b   cg   b   e   e   ch

Refine Search

Search Results -

Terms	Documents
L3	0

Database:

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:

```
L4
```

[Refine Search]

[Recall Text]   [Clear]   [Interrupt]

Search History

DATE: Tuesday, April 27, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L4	L3	0	L4
DB=USPT; PLUR=YES; OP=OR			
L3	(memory near2 controller) same (switch$3 near5 path) same (enabl$3 or disabl$3)	21	L3
L2	(memory near2 controller) same (switch$3 near5 path) same enabl$3	21	L2
L1	(memory near2 controller) same (switch$3 near5 path) same enabl$3 same disabl$3	0	L1

END OF SEARCH HISTORY

h   e   b   b   cg   b   e   e   ch

Refine Search

Search Results -

Terms	Documents
L13 and (enabl$3 same disabl$3 same path)	11

Database:
```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search: L15

[Refine Search]

[Recall Text] [Clear] [Interrupt]

Search History

DATE: Tuesday, April 27, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L15	L13 and (enabl$3 same disabl$3 same path)	11	L15
L14	L13 and (enabl$3 same disabl$3)	90	L14
L13	(710/316.ccls. or 710/317.ccls.)	541	L13
L12	L6 and (710/316.ccls. or 710/317.ccls.)	3	L12
L11	L6 and (710/316,317.ccls.)	3	L11
L10	L9 same controller	4	L10
L9	l6 same memory	61	L9
L8	L6.ab.	22	L8
L7	L6 same backplane	2	L7
L6	(switch$3 near5 path) same enabl$3 same disabl$3	538	L6
L5	(first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch$3 near5 path)	1	L5
DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			

h   e b   b cg b   e e ch

L4	L3	0	L4

DB=USPT; PLUR=YES; OP=OR

L3	(memory near2 controller) same (switch$3 near5 path) same (enabl$3 or disabl$3)	21	L3
L2	(memory near2 controller) same (switch$3 near5 path) same enabl$3	21	L2
L1	(memory near2 controller) same (switch$3 near5 path) same enabl$3 same disabl$3	0	L1

END OF SEARCH HISTORY

h   e b   b cg b   e e ch

Refine Search

Search Results -

Terms	Documents
(first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch$3 near5 path)	1

Database:

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search: L5

[Refine Search]

[Recall Text] [Clear] [Interrupt]

Search History

DATE: Tuesday, April 27, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L5	(first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch$3 near5 path)	1	L5
DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L4	L3	0	L4
DB=USPT; PLUR=YES; OP=OR			
L3	(memory near2 controller) same (switch$3 near5 path) same (enabl$3 or disabl$3)	21	L3
L2	(memory near2 controller) same (switch$3 near5 path) same enabl$3	21	L2
L1	(memory near2 controller) same (switch$3 near5 path) same enabl$3 same disabl$3	0	L1

END OF SEARCH HISTORY

h   e b   b   cg b   e e ch

Refine Search

Search Results -

Terms	Documents
L6 same backplane	2

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L7

[Refine Search]

[Recall Text ⬍]   [Clear]   [Interrupt]

Search History

DATE: Tuesday, April 27, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L7	L6 same backplane	2	L7
L6	(switch$3 near5 path) same enabl$3 same disabl$3	538	L6
L5	(first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch$3 near5 path)	1	L5
DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L4	L3	0	L4
DB=USPT; PLUR=YES; OP=OR			
L3	(memory near2 controller) same (switch$3 near5 path) same (enabl$3 or disabl$3)	21	L3
L2	(memory near2 controller) same (switch$3 near5 path) same enabl$3	21	L2
L1	(memory near2 controller) same (switch$3 near5 path) same enabl$3 same disabl$3	0	L1

h   e b   b cg b   e e ch

Refine Search

Search Results -

Terms	Documents
L6.ab.	22

Database:

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search: L8

[Refine Search]

[Recall Text] [Clear] [Interrupt]

Search History

DATE: Tuesday, April 27, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L8	L6.ab.	22	L8
L7	L6 same backplane	2	L7
L6	(switch$3 near5 path) same enabl$3 same disabl$3	538	L6
L5	(first adj1 memory adj1 controller) same (second adj1 memory adj1 controller) same (switch$3 near5 path)	1	L5
DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L4	L3	0	L4
DB=USPT; PLUR=YES; OP=OR			
L3	(memory near2 controller) same (switch$3 near5 path) same (enabl$3 or disabl$3)	21	L3
L2	(memory near2 controller) same (switch$3 near5 path) same enabl$3	21	L2
L1	(memory near2 controller) same (switch$3 near5 path) same enabl$3 same disabl$3	0	L1

h   e   b   b   cg   b   e   e   ch